

NOTES, UNLESS OTHERWISE SPECIFIED:

1. The netname "TEC_V1P0VREF" represents connection to the +1.0V power buss.
2. The netname "TEC_V2P5VREF" represents connection to the +2.5V power buss.
3. The netname "P3P3V" represents connection to the +3.3V power plane.
4. The netname "P5P0V_ANA" represents connection to the +5.0V power buss.
5. The netname "P6P2V" represents connection to the +6.2V power buss.
6. The netname "P12V_SUPPLY" represents connection to the +12.0V power buss.
7. The netname "GND" represents connection to the ground plane.
8. A "Z" suffix on a signal name indicates an active low signal.
9. All components with designators "U*", "Q*", and "D*" are electrostatic discharge sensitive.
10. All resistor values are in ohms.
11. All capacitor values in microfarads unless otherwise specified.



COMPUTER GENERATED DRAWING - DO NOT REVISE MANUALLY

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	ECO 2136146: Initial Release	09/23/2013	HPC
B	ECO 2138100: R3 & R5 reversed, Add R12, U5-1&10 reversed	12/06/2013	GW
C	ECO 2140599: Move U1-13 from 12V_Supply net to SITARA_TEC_EN net	02/14/2014	GW

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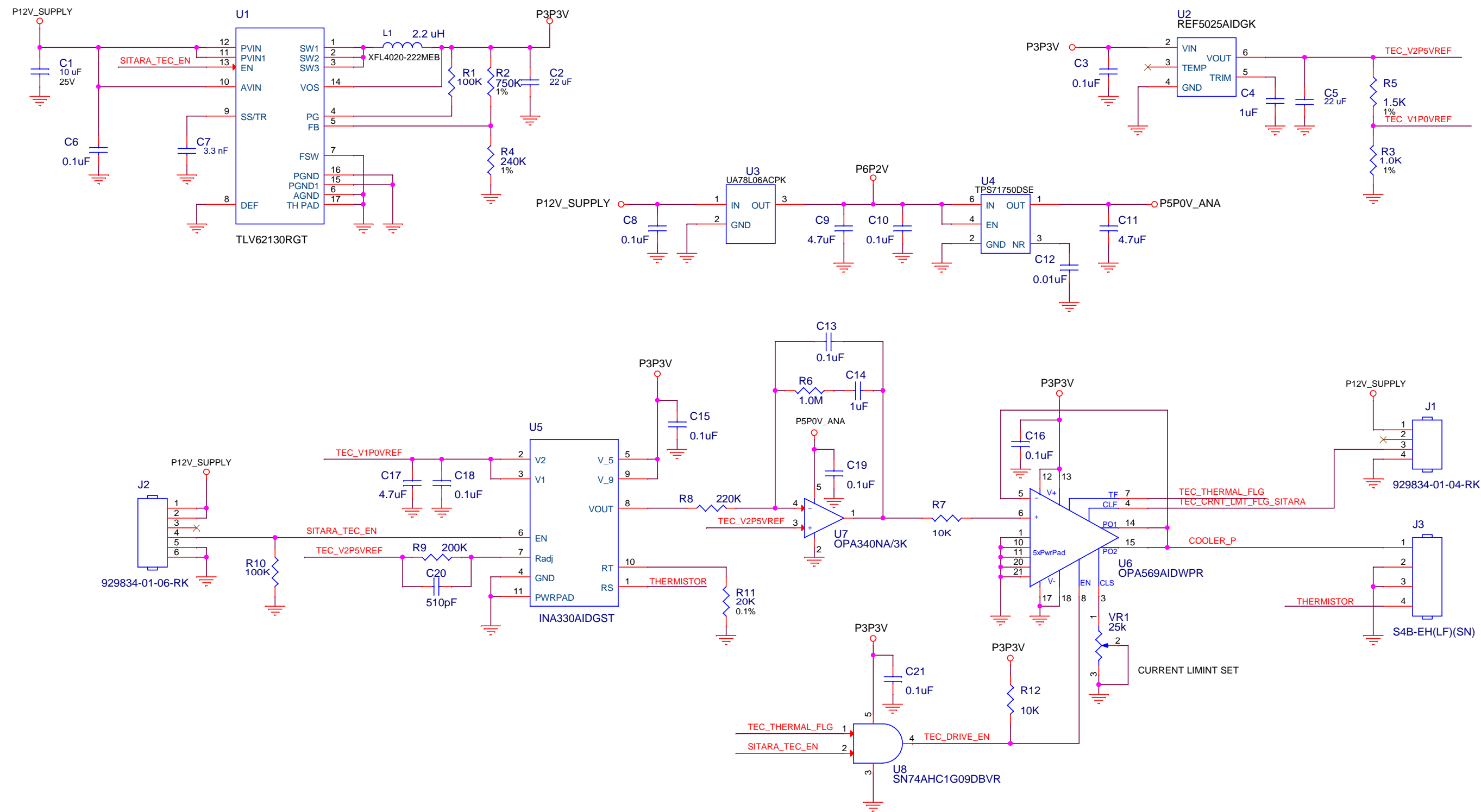
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		ENGR Eric Pruett				
		APVD Joe Siddall				
		MFG		TITLE ESD, NIRscan TEC Driver Board		
	0314CP	QA				
NEXT ASSY	USED ON			A3	DRAWING NO 2513471	REV C
APPLICATION		SW Allegro Design Entry 16.6			SCALE	SHEET 1 of 3



Revision History

Rev. A: Initial Release ~ 11/25/2013

Rev. B: ECO 2138100: R3 & R5 reversed, Add R12, U5-1&10 reversed ~ 12/06/2013

Rev. C: ECO 2140599: U1-13 moved from P12V_Supply net to SITARA_TEC_EN net
Up-date title block, disclaimer
Removed "SPECIAL ROUTING INSTRUCTIONS" note ~ 02/14/2014

Schematic Revision History

TEXAS INSTRUMENTS	DWN P. Cleveland	DATE 09/23/2013	A3	DRAWING NO 2513471		REV C
	ISSUE DATE			SCALE		SHEET 3 OF 3

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